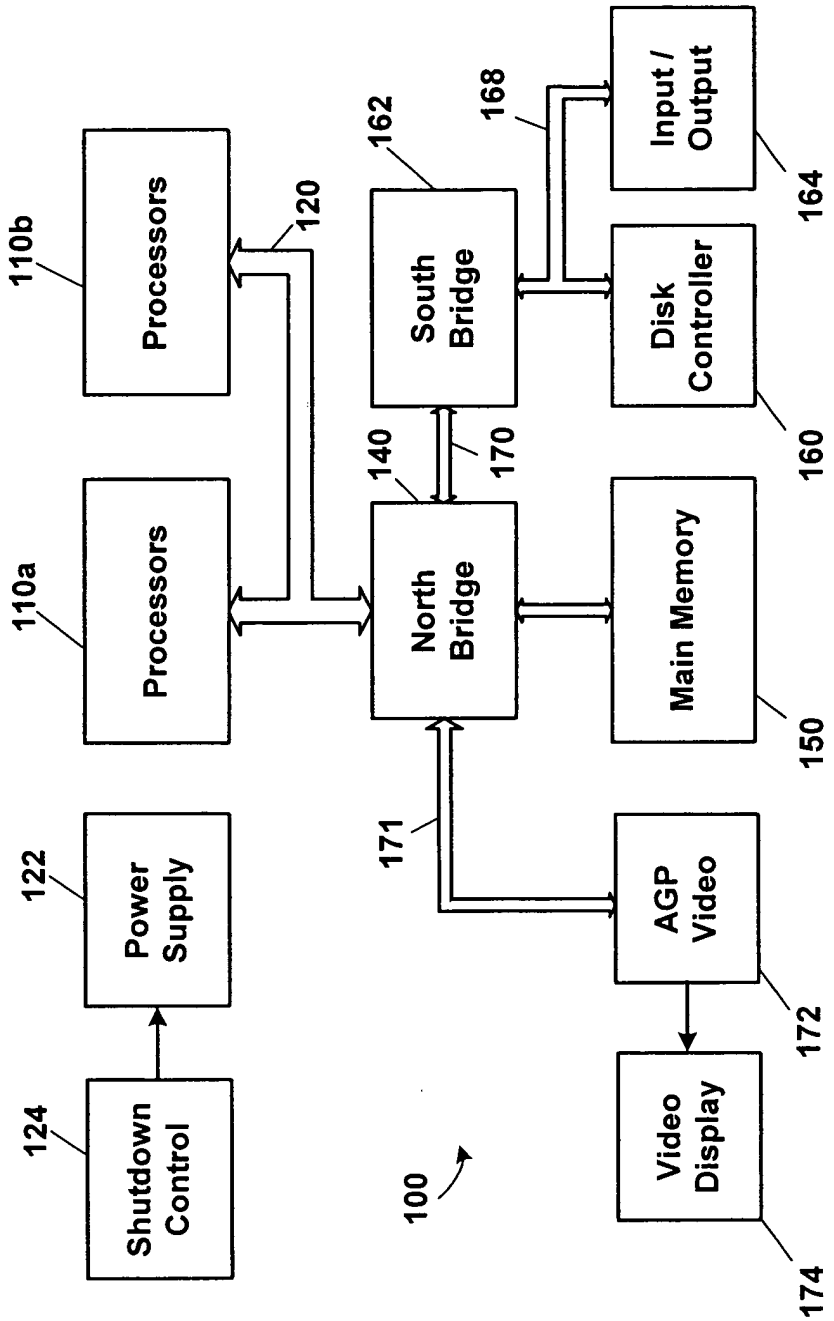
Processors
110b
120
Processors
110a
South Bridge
162
168
Input / Output
164
Disk Controller
160
140
170
North Bridge
Main Memory
150
171
AGP Video
172
Video Display
174
Power Supply
122
Shutdown Control
124
100

Figure 1

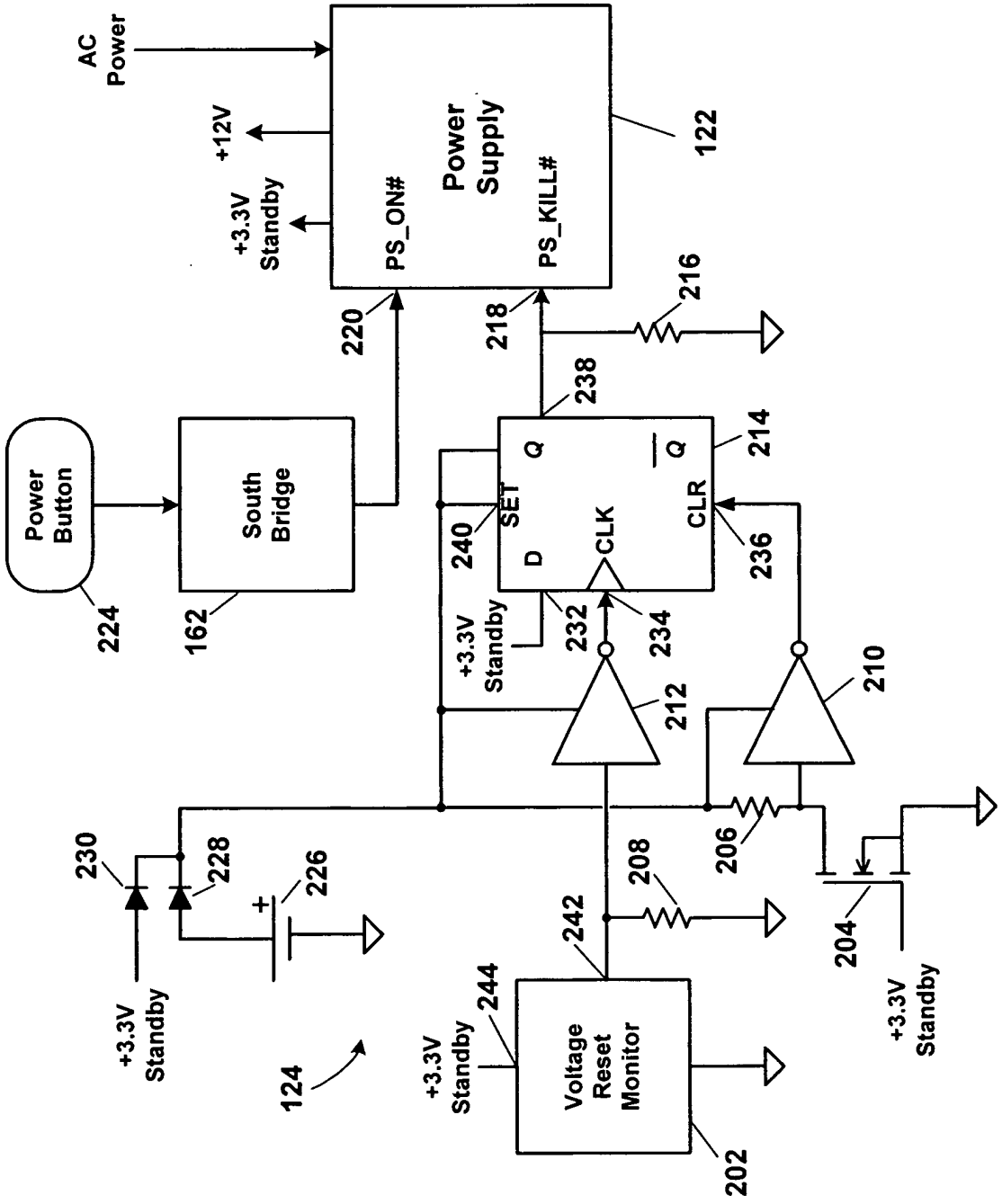
AC Power
+12V
+3.3V Standby
Power Supply
PS_ON#
PS_KILL#
122
220
218
216
238
Power Button
South Bridge
224
162
240
SET
Q
D
CLK
CLR
Q
214
236
+3.3V Standby
232
234
212
210
230
228
226
206
208
242
204
+3.3V Standby
+3.3V Standby
124
+3.3V Standby
244
Voltage Reset Monitor
202

Figure 2

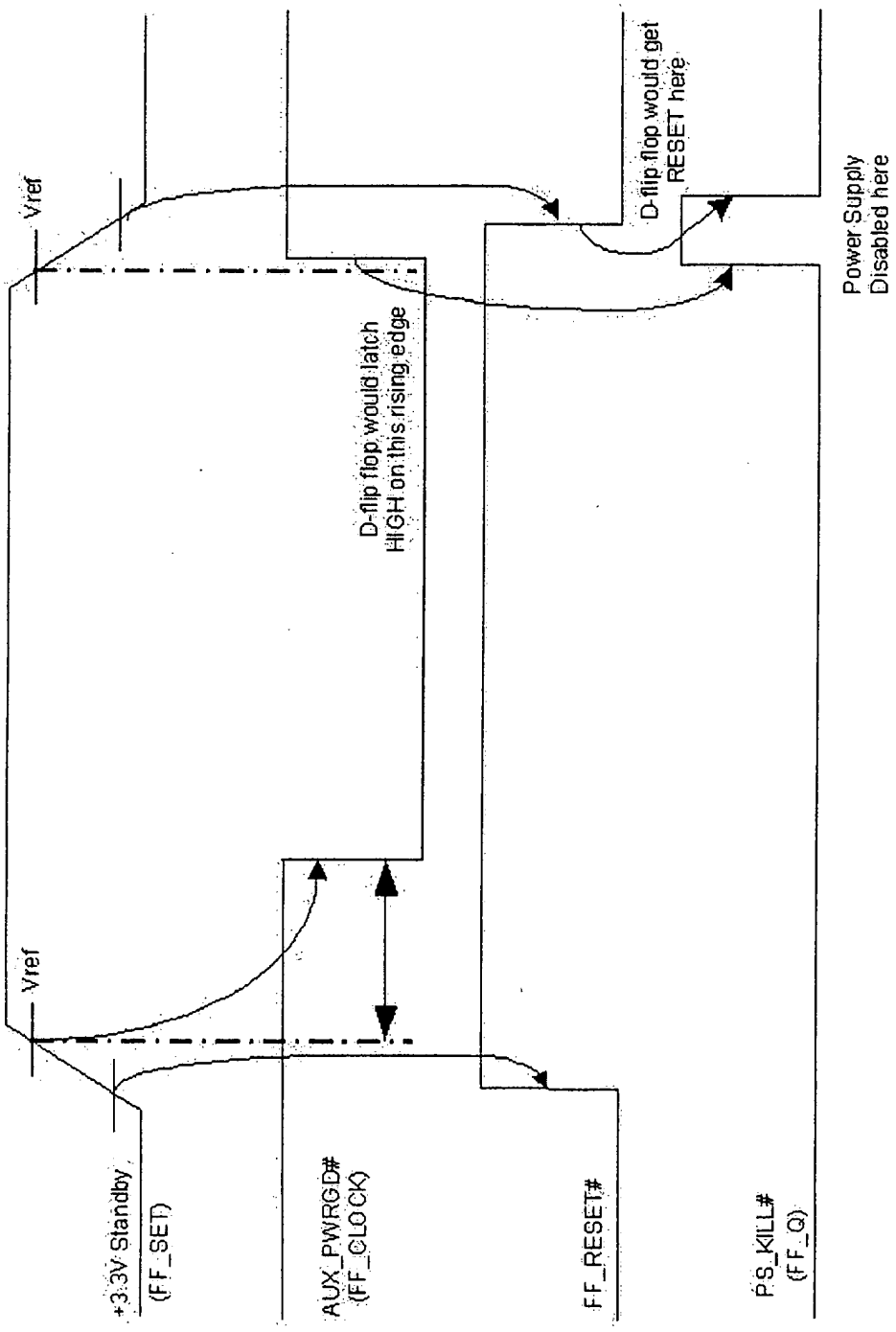
Vref
Vref
D-flip flop would latch
HIGH on this rising edge
D-flip flop would get
RESET here
Power Supply
Disabled here
+3.3V Standby
(FF_SET)
AUX_PWRGD#
(FF_CLOCK)
FF_RESET#
PS_KILL#
(FF_Q)

FIGURE 3